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| --- | --- |
| Group Name | 1 |
| Project Name | Custom Encryption and Decryption Algorithm |
| Project Manager | Srinivas |
| Documented by | L S V Prasanthi  T V K S Prasanthi  G Kalavathi  A Lavanya  N SuryaPrabha |
| Inspected on |  |
| Inspected by | Srinivas |

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|  |  |  | **Design Review Checklist** |  |  |
|  | **S. No.** | **Category** | **Item** | **Yes/No/NA** | **Remarks** |
|  |  |  |  |  |  |
|  |  | **Tracible** |  |  |  |
|  | 1.1 |  | Are all requirements addressed in the design? | **Yes** |  |
|  | 1.2 |  | Does design capture feature or function level design/flows/data structures for meeting functional requirements? | **Yes** |  |
|  | 1.3 |  | Are all the (external and internal) interfaces designed | **Yes** |  |
|  | 1.4 |  | Have the performance requirements been addressed? | **Yes** |  |
|  | 1.5 |  | The design addresses security | **Yes** |  |
|  | 1.6 |  | The design ensures that the scalability requirements, if any, are addressed | **Yes** |  |
|  | 1.7 |  | The software (component) design addresses porting requirements | **Yes** |  |
|  | 1.8 |  | The software (component) design considers the maintainability requirements | **Yes** |  |
|  | 1.9 |  | The design does not violate any design constraints documented in the SRS | **No** |  |
|  | 1.1 |  | The design does not violate any requirement | **Yes** |  |
|  | 1.11 |  | All stated and unstated (implied) requirements are addressed | **Yes** |  |
|  | 1.12 |  | Has the updated RTM been sent as part of deliverables for review? (RTM should also be a part of the deliverable for review) | **NA** |  |
|  |  |  |  |  |  |
|  |  | **Complete** |  |  |  |
|  | 2.1 |  | All Applicable sections are filled as per template | **Yes** |  |
|  | 2.2 |  | The design addresses response of the software (module) to all erroneous response from the software (module) |  |  |
|  | 2.3 |  | The design addresses response of the software (module) to all valid (and invalid) input data as defined in the SRS/any other requirement specification document |  |  |
|  | 2.4 |  | Has an alternate design approach been considered? | **No** |  |
|  | 2.5 |  | Is the alternate design approach documented in the document? | **NA** |  |
|  | 2.6 |  | Is the rationale for following an approach documented? | **NA** |  |
|  |  |  |  |  |  |
|  |  | **Accurate** |  |  |  |
|  |  |  | All assumptions made during the design been stated clearly | **Yes** |  |
|  | 3.1 |  | Are all design assumptions validated | **Yes** |  |
|  | 3.2 |  | The design does not impose any new requirements | **Yes** |  |
|  |  | **Consistency** |  |  |  |
|  | 4.1 |  | There is no conflict within the design sections | **No** |  |
|  |  |  |  |  |  |
|  |  | **Detailed** |  |  |  |
|  | 5.1 |  | The Inter process communication protocol, if required, is defined | **Yes** |  |
|  | 5.2 |  | The design is understandable | **Yes** |  |
|  |  |  |  |  |  |
|  |  | **Design Reuse** |  |  |  |
|  | 6.1 |  | Has any part of the design reused | **No** |  |
|  |  |  |  |  |  |
|  |  | **Capacity and Scalability** |  |  |  |
|  | 8.1 |  | Has the main data structures which would occupy major RAM memory been identified? | **No** |  |
|  | 8.2 |  | Has the capacity values for the main data structures which would occupy major RAM been identified? | **No** |  |
|  | 8.3 |  | Has the total memory used for static storage as well as transactional memory been theoretically calculated for Max resources | **No** |  |
|  | 8.4 |  | Is the memory requirement for the system within limits (both for 64bit as well as 32bit)? | **Yes** |  |
|  | 8.5 |  | Does the design take care to allocate memory for the resources at init time? | **Yes** |  |
|  |  | |  |  |  |